

PTO/SB/08 Equivalent

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Multiple sheets used when necessary)

SHEET 1 OF 1

Application No.	09/886,533
Filing Date	June 20, 2001
First Named Inventor	Ha et al.
Art Unit	2122
Examiner	Chuck O. Kendall
Attorney Docket No.	IMEC214.001AUS

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
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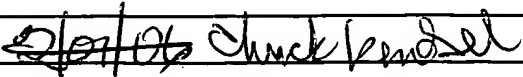
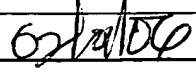
FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code-Number-Kind Code Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ¹
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
OK	1	European International Search Report. Dated March 1, 2005.	
OK	2	Guccione, Steve, et al., "JBits: Java based interface for reconfigurable computing," presented at the 2 nd Annual Military Aerospace Applications of Programmable Devices and Technologies Conference, MAPLD 1999, Sept. 28-30, 1999.	
OK	3	HA et al., "A Scalable Architecture to Support Networked Reconfiguration," ProRISC 99. 10 19-01:101. 677-83, November 24, 1999.	
OK	4	Ha, Yajun, et al., "Building a Virtual Framework for Networked Reconfigurable Hardware and Software Objects," presented at International Conference on Parallel and Distributed Processing Techniques and Applications 2000, PDPTA 2000, June 24-29, 2000.	
OK	5	FLEISCHMANN et al., "A Hardware/Software Prototyping Environment for Dynamically Reconfigurable Embedded Systems." International Conference on Hardware Software Codesign, Seattle, Washington, Proc. Of the 6 th International Workshop on Hardware/Software codesign, pp.105-109, March 1998.	

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Examiner Signature		Date Considered	
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

T¹ - Place a check mark in this area when an English language Translation is attached.